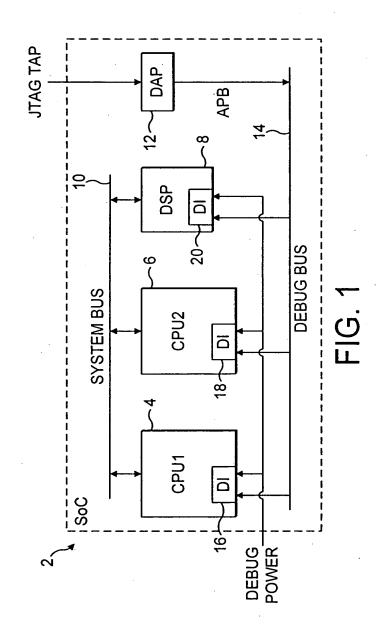


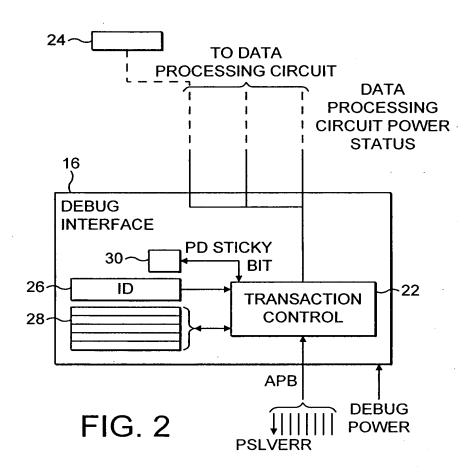
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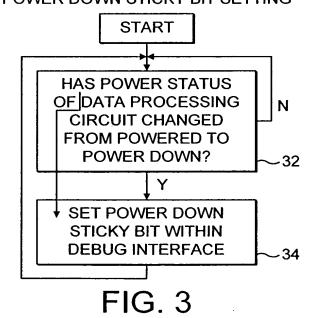
Inventor: ALLUE et al SN 10/801,131/Sheet 2 of 3 Atty. Dkt.: 550-532



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POWER DOWN STICKY BIT SETTING





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DEBUG BUS ERROR GENERATION

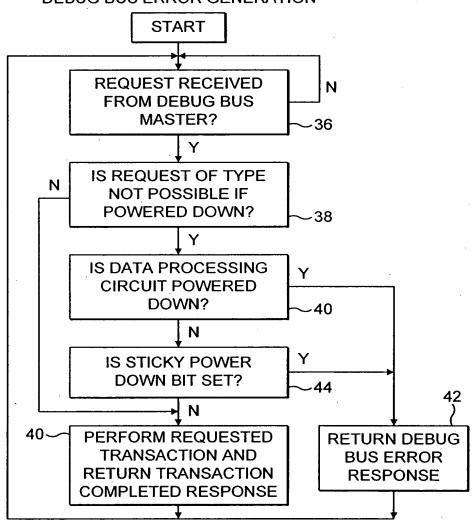


FIG. 4